

# ADSL Transformer

## Designed for use with ADI chip set

SPECIFICATIONS :

ELECTRICAL :

IMPEDANCE : DESIGNED TO REFLECT 100 OHMS ON THE PRI. WITH 25 OHMS LOAD ON SEC.

D.C. RESISTANCE : 10-8 = 0.75 OHMS MAX.

9-7 = 0.75 OHMS MAX.

1-3 = 0.20 OHMS MAX.

2-4 = 0.25 OHMS MAX.

INDUCTANCE : 1.2 mH  $\pm$  10% @ 20KHz , 1VAC , 0mADC , 10-7(TIE 8+9,2+3)

TURNS RATIO : (10-7):(1-4),(2.0):(1.00) , TIE(8+9,2+3)  $\pm$  2%

LEAKAGE INDUCTANCE : 10.0  $\mu$ H MAX. 100KHz, 100mVAC , 10-7 (TIE 8+9,2+3,1+4 SHORTED)

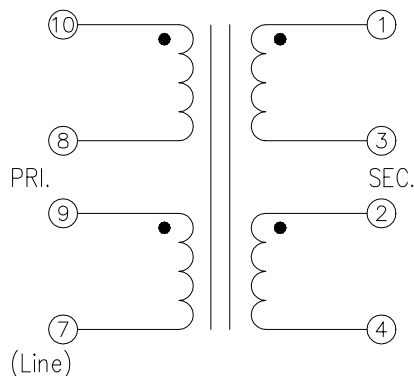
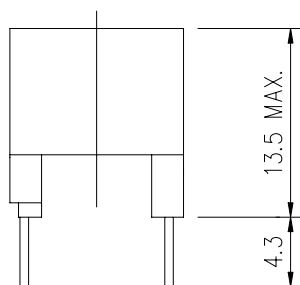
T.H.D. : -75 dB MAX. @ 20KHz, 4Vrms ACROSS PRI. 25 OHMS LOAD, 100 OHMS INPUT, TIE(8+9,2+3)

INSERTION LOSS : 0.5 dB MAX. @ 300KHz

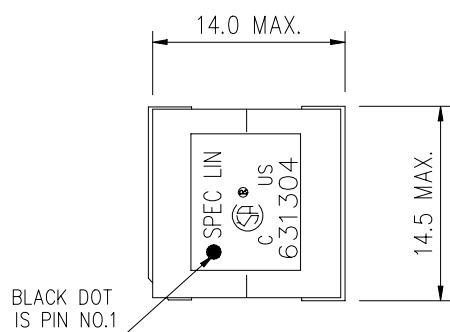
INTERWINDING CAPCITANC : 50 pF MAX. @ 10KHz , 1VAC , 10-1 , TIE(8+9,2+3), Cs

LONGITUDINAL BALANCE : 46 dB MIN. @ 10KHz-1MHz , PER ITU MEHOD (L > M)  
WITH TERMINAL 1 GROUNDED

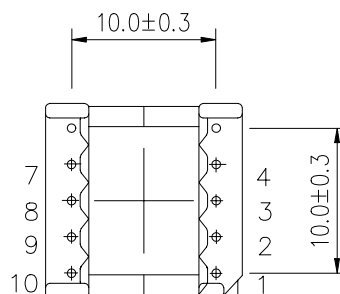
HI-POT TEST : 1875 VAC FOR 1 SECOND BETWEEN PINS 10-1(TIE 8+9,2+3)



SCHEMATIC



TOP VIEW



BOTTOM VIEW

**SPEC LIN ENTERPRISE CO. LTD.**

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AGENCY NO.

UL NO. : E181559

CSA NO. 186519

ISO 9002 : 036809750

DRAWING NO.

**631304**

UNITS  
mm

SHEET  
1 OF 1

