

HDSL Transformer For Brooktree and Level One Chipsets

SPECIFICATIONS :

ELECTRICAL :

D.C. RESISTANCE : LINE = 4.4 OHMS MAX.(6-9) TIE(7+8) @ 25°C

CHIP = 2.2 OHMS MAX.(2-4)

TURN RATIO : (CHIP : LINE) = 1CT : 1: 1 ± 2%

D.C. CURRENT : 70 mA MAX. IN LINE

INDUCTANCE : 6-9 = 2.0mH ± 6% @ 10KHz, 0.1Vrms,Ls

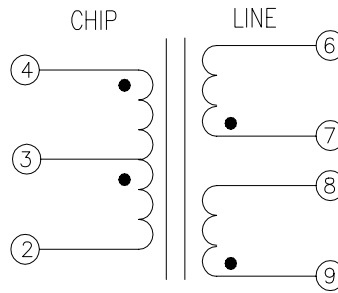
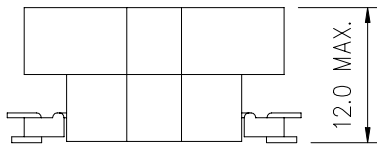
RETURN LOSS : 16.5 dB MIN. @ 40KHz - 320KHz

CHIP WITH 33.7 OHMS LOAD, REF. 135 OHMS ON LINE

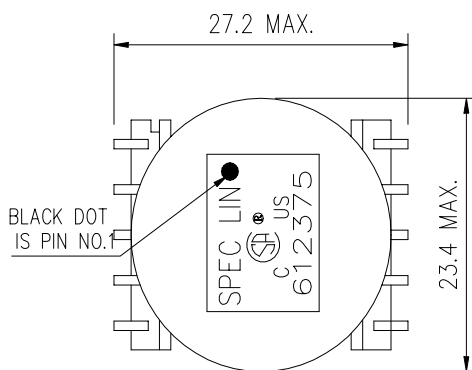
LONGITUDINAL BALANCE : 55 dB TYP. @ 292KHz

THD : -75 dB TYP. @ 200KHz , 6V p-p

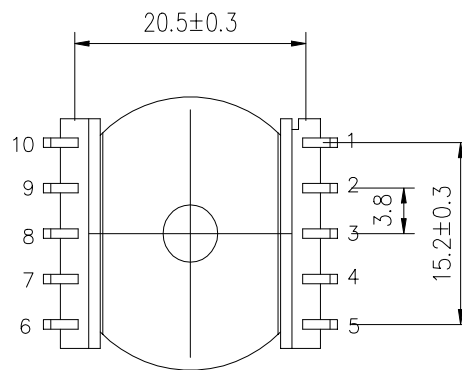
HI-POT TEST : 1.50 KVAC MIN. BETWEEN LINE & CHIP 1mA FOR 2 SECONDS



SCHEMATIC



TOP VIEW



BOTTOM VIEW

CHECKBY: _____

DESIGN : _____

DRAWER : _____

SPEC LIN ENTERPRISE CO. LTD.

2F, No.6, ALLEY 17, LANE 42, CHUNG HSING N. ST.

SAN CHUNG CITY, TAIPEI, TAIWAN

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AGENCY NO.

UL NO. : E181559

CSA NO. : LR107869

ISO 9002: 036809750

DRAWING NO.

612375

UNITS
mm

SHEET
1 OF 1

