

HDSL Transformer For Brooktree and Level One Chipsets

SPECIFICATIONS :

ELECTRICAL :

D.C. RESISTANCE : 6-8 = 3.0 OHMS MAX.@ 25°C

8-10 = 3.0 OHMS MAX.

2-4 = 3.2 OHMS MAX.

TURN RATIO : (CHIP : LINE) = 1 : 1.8 ± 2%

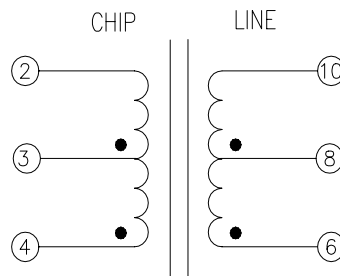
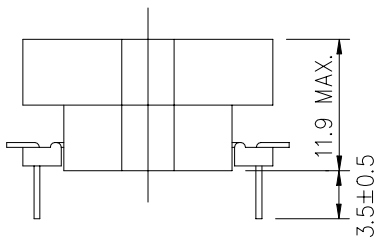
INDUCTANCE : 6-10 = 2.75 mH ± 6% @ 10KHz, 0.1Vrms Ls 75mA

RETURN LOSS : 18 dB MIN. @ 40KHz - 200KHz

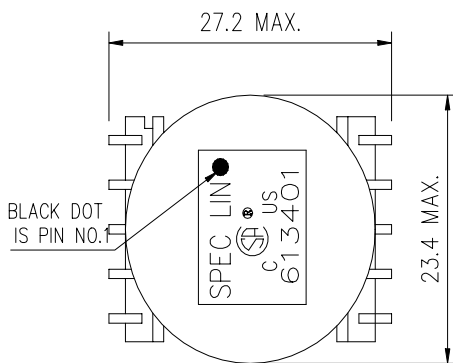
LONGITUDINAL BALANCE : 50 dB MIN. @ 5KHz - 196KHz , 135 OHMS

T.H.D : -75 dB TYP.@ 5KHz 6V p-p , 135 OHMS

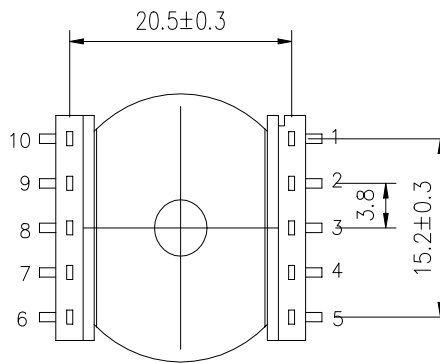
HI-POT TEST : 2.00 KVAC MIN. BETWEEN LINE & CHIP 1mA FOR 2 SECONDS



SCHEMATIC



TOP VIEW



BOTTOM VIEW

CHECKBY: _____

DESIGN : _____

DRAWER : _____

SPEC LIN ENTERPRISE CO. LTD.

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SAN CHUNG CITY, TAIPEI, TAIWAN
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AGENCY NO.

UL NO. : E181559

CSA NO. : LR107869

ISO 9002 : 036809750

DRAWING NO.

613401

UNITS
mm

SHEET
1 OF 1

