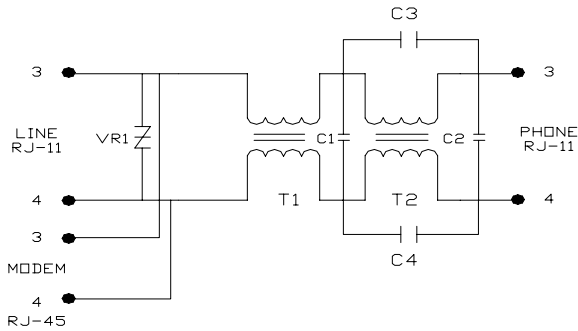


ADSL POTS SPLITTER

- Designs exceed standard for ITU-T G.992,1
- POTS loop DC currents from 0mA to 100mA
- Excellent longitudinal balance
- Operation temperature -10 to 75°C
- Low Cost



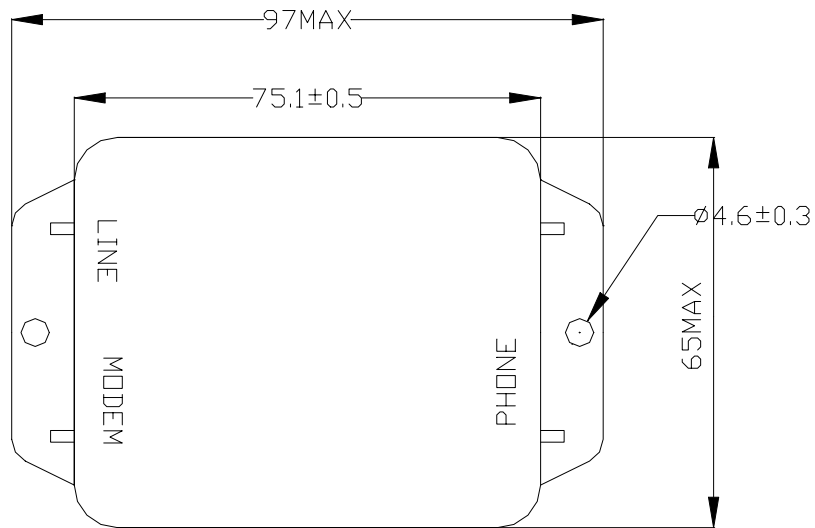
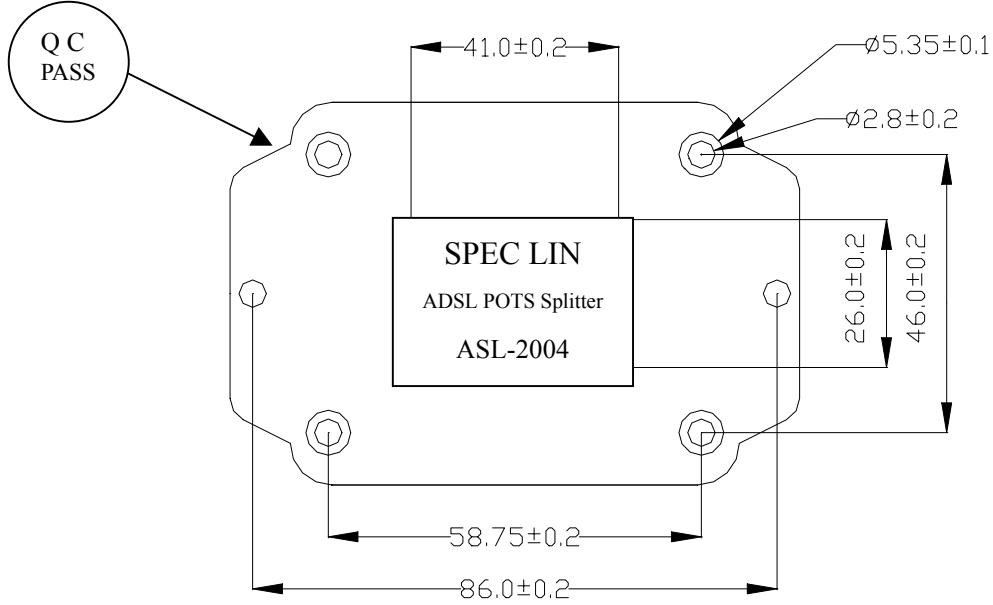
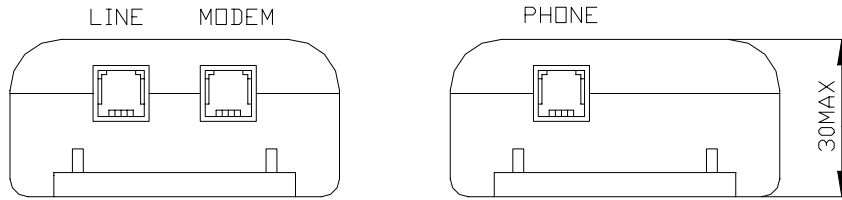
Specifications

		MIN.	TYP.	MAX.	Unit
Insertion loss	1004Hz short loop			1.0	dB
	1004Hz long loop			0.75	dB
Attenuation distortion	200Hz to 3.4KHz short loop	-1.5		1.5	dB
	3.4KHz to 4KHz short loop	-2.0		2.0	dB
	200Hz to 3.4kHz long loop	-1.5		0.5	dB
	3.4KHz to 4KHz long loop	-1.5		1.0	dB
Attenuation in ADSL band	30KHz to 300KHz	65			dB
	300KHz to 1104KHz	55			dB
Delay distortion	600Hz to 3.2KHz short loop			200	usec
	200Hz to 4KHz short loop			250	usec
	600Hz to 3.2KHz long loop			200	usec
	200Hz to 4KHz long loop			250	usec
Return loss @Short Loop & Long Loop	ERL	6			dB
	SRL – LO	5			dB
	SRL – HI	3			dB
Longitudinal balance	200Hz to 3200Hz 25mA	52			dB
Tip to ring Capacitor	@30Hz	20		115	nF
DC resistance	Tip to ring			25	ohms
	Tip or ring to ground	5			Mohms
DC current			100		mA

SPEC LIN ENTERPRISE CO.LTD.
AGENCY NO. ISO 9002 : 036809750

UNITS: mm
 SHEET: 1 OF

DRAWING NO.
ASL-2004



<p>SPEC LIN ENTERPRISE CO. LTD.</p> <p>BLOCK2,LIU WU FIRST INDUSTRIALAREA,ERHEN RO. WE, SHIH-JE TOWN,DONG GUAN CITY,KUAN DONG PROVINCE CHINA TEL:86-769-632-1088 FAX:86-769-632-1061</p>	<p>AGENCY NO:</p> <p>ISO 9002 : 036809750</p>	DRAWING NO.		
		ASL-2004		
		UNITS mm	SHEET OF	

SPEC LIN ENTERPRISE CO.,LTD.

ELECTRICAL CHARACTERISTICS

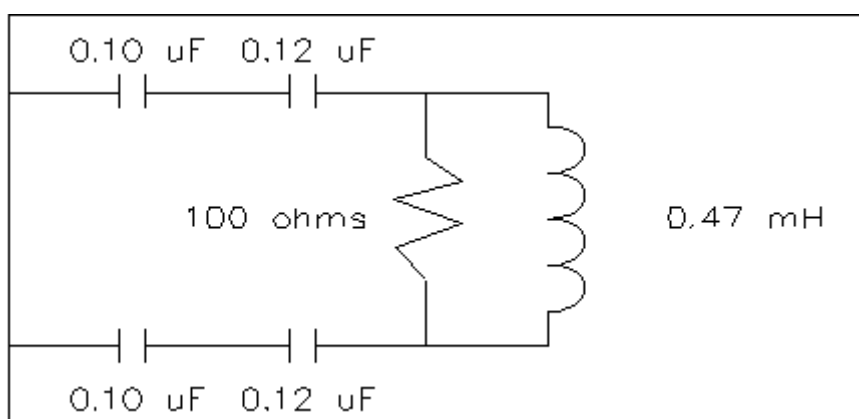
1. Technical Requirement:

1.1 ZHP-r Defined

To facilitate testing of the POTS splitter independently of the actual modem or specific

vendor. ZHP-r is defined in Figure E-1 to allow proper termination of xDSL port during. The ZHP is valid only for voice band frequencies. The combination of capacitors in the voice band testing ZHP-r is only representative.

ZHP Remote end



NOTE Component Tolerances: Capacitors:2.5%, Resistors: 1%, Coils: 5%

Figure E-1 ZHP-r definitions

1.2 Tset Loops

Loops to be used for testing are divided into two groups. This is done to obtain more specific requirements under the widely varying conditions of short and long loops and to account for the effect of the opposite splitter impedances being "seen" through the loop and affecting performance.

▶ Short Loops: 0, 0.5 Kft, 2.0 Kft 5 Kft pairs of 26 AWG cables

▶ Long Loops: ANSI T1.601 resistance design loops 7, 9, and 13 and T1

TR28 CSA loops 4, 6, 7 and 8